

Fig. 2

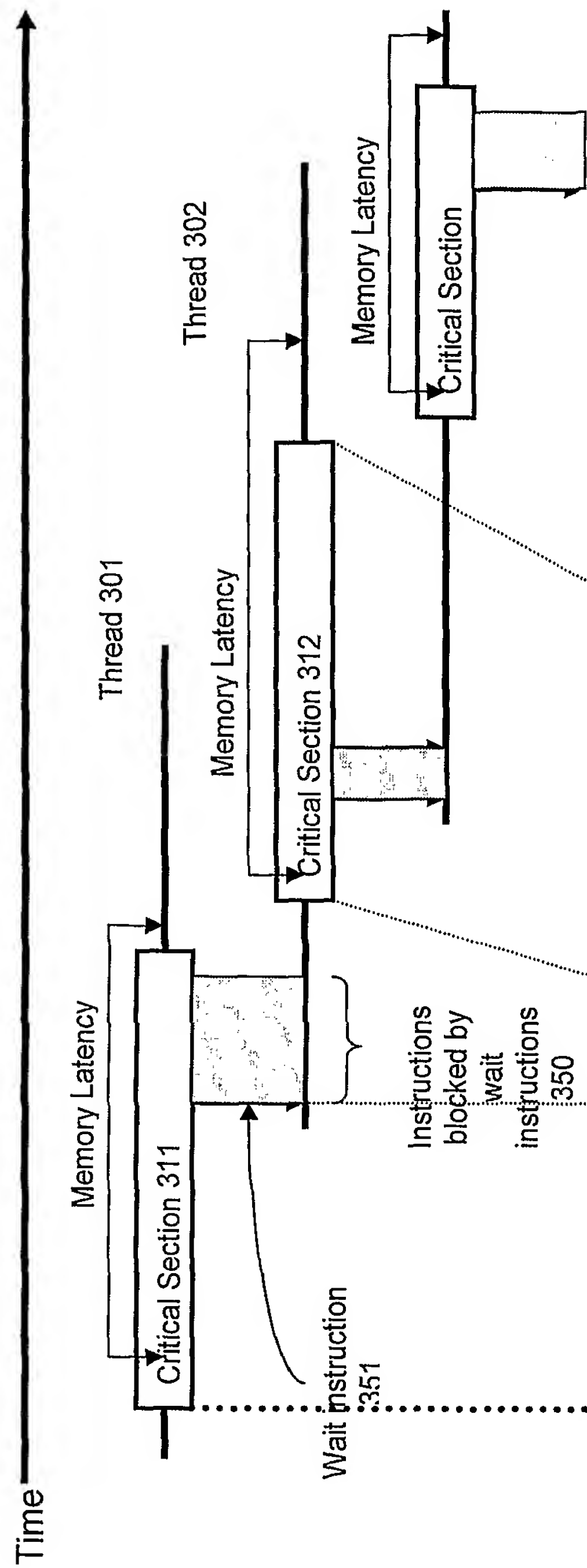


Fig. 3a

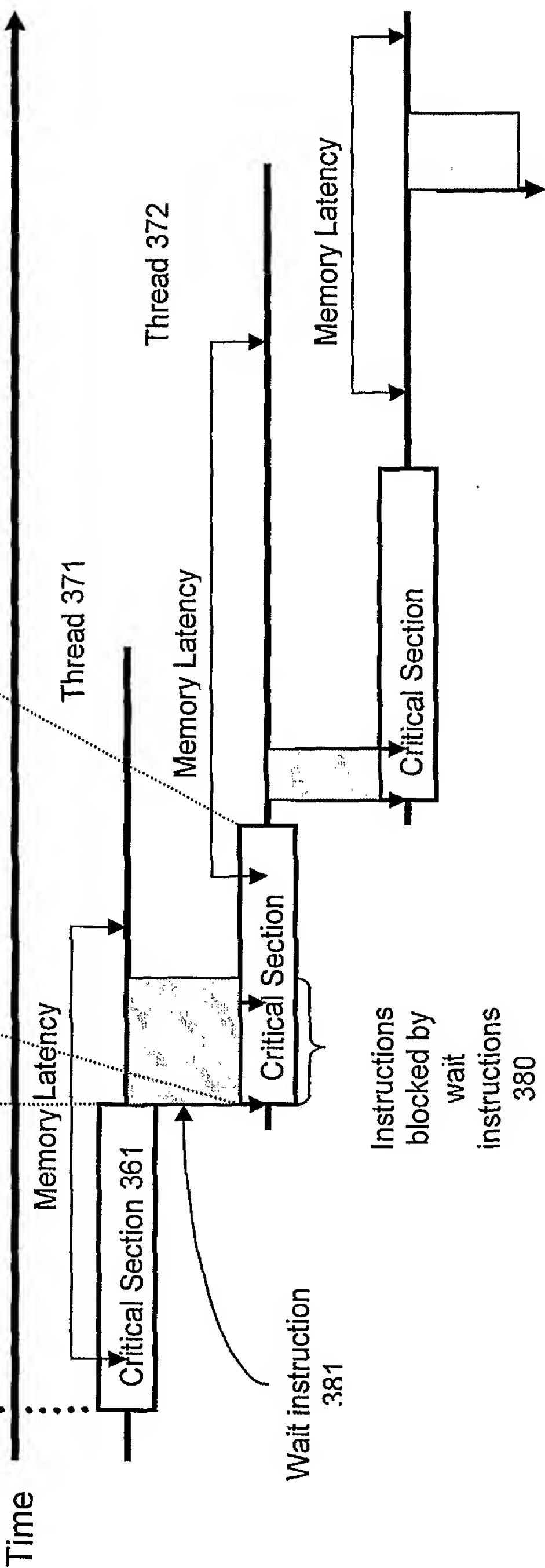
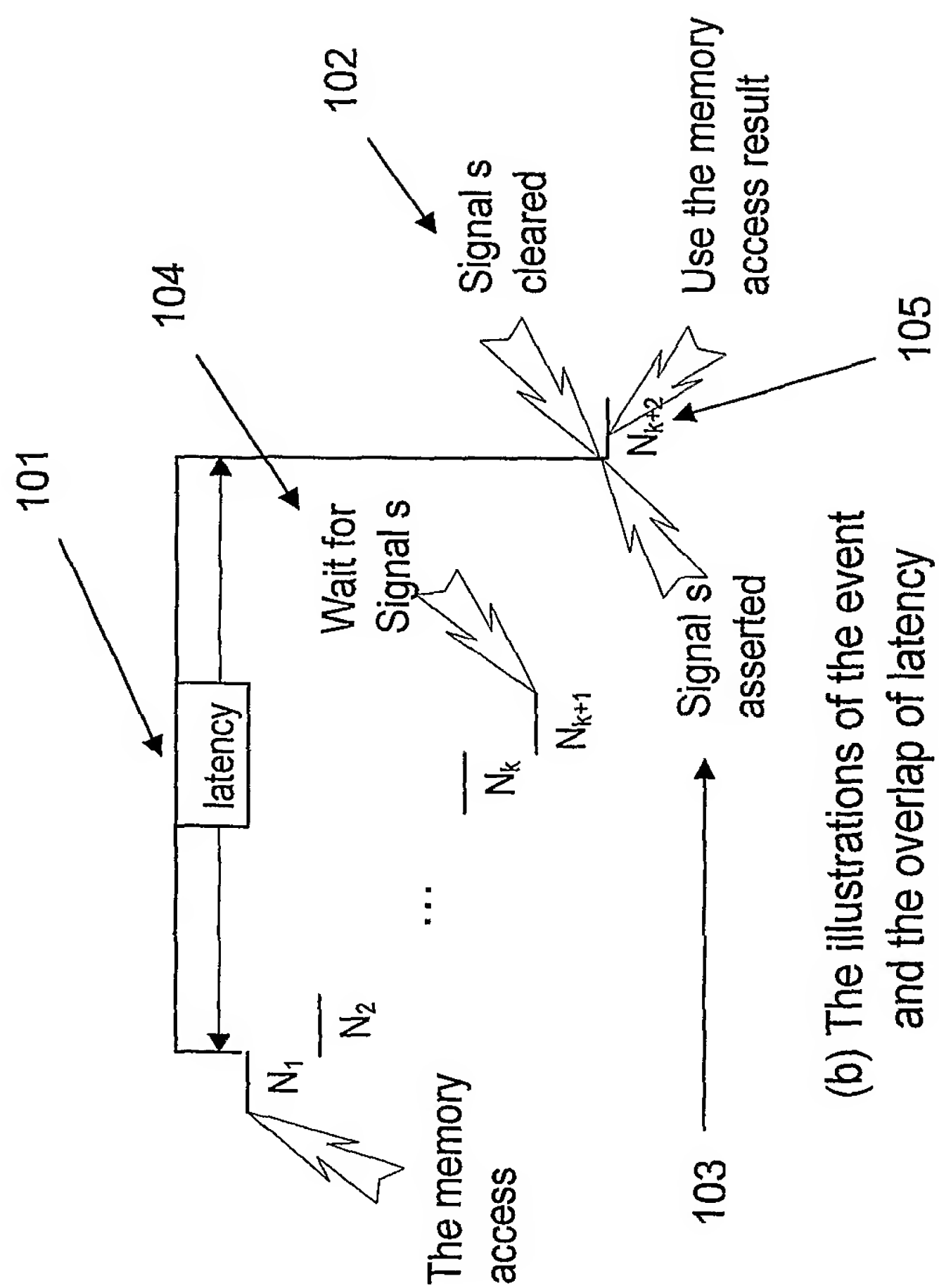


Fig. 3b



(४)

$$\begin{array}{l} N_1: R1 = \text{load}[R2], \text{signals} \\ N_2: R3 = R2 + 1 \\ \dots \dots \\ N_k: R4 = R3 < 2 \\ N_{k+1}: \text{wait s} \\ N_{k+2}: R5 = R1 - R4 \end{array}$$

Figs. 1a-b

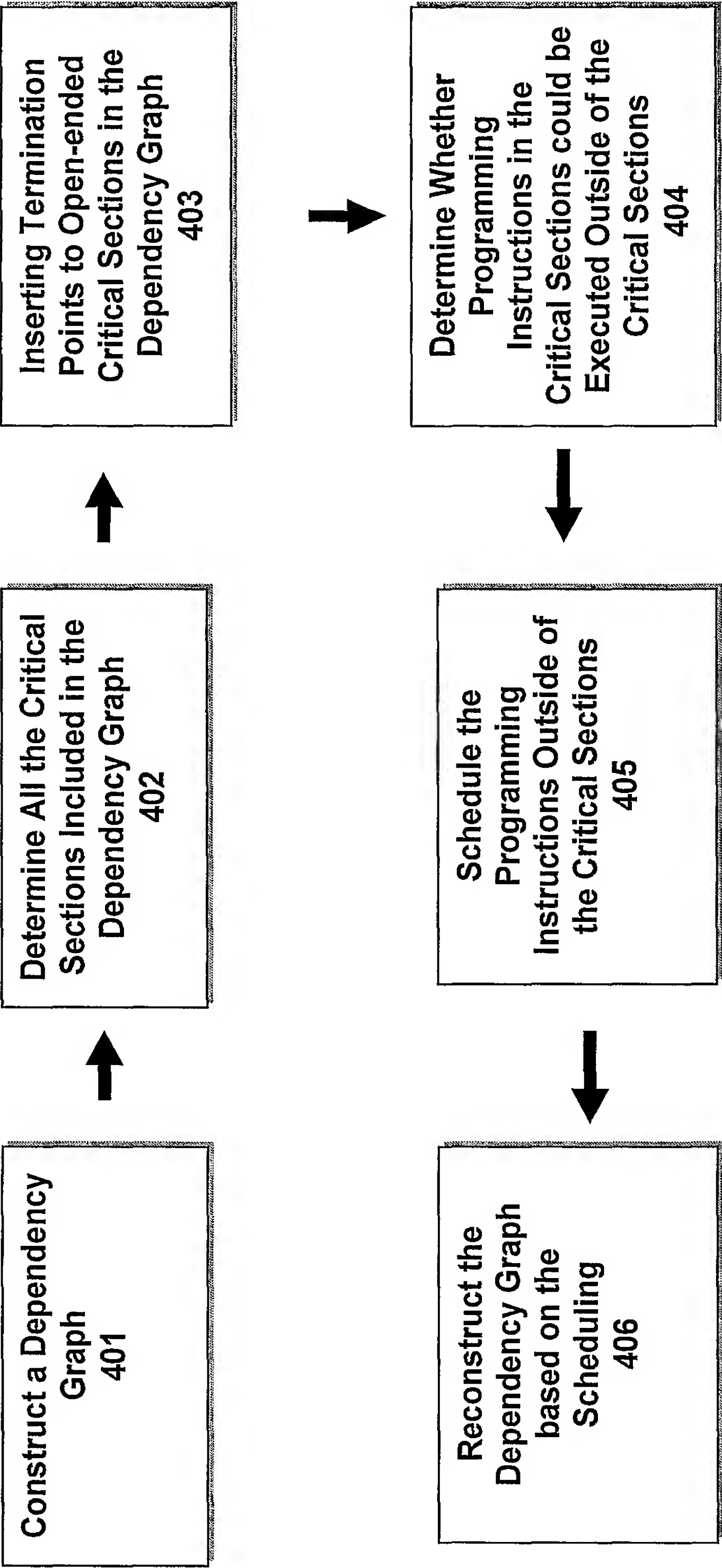


Fig. 4

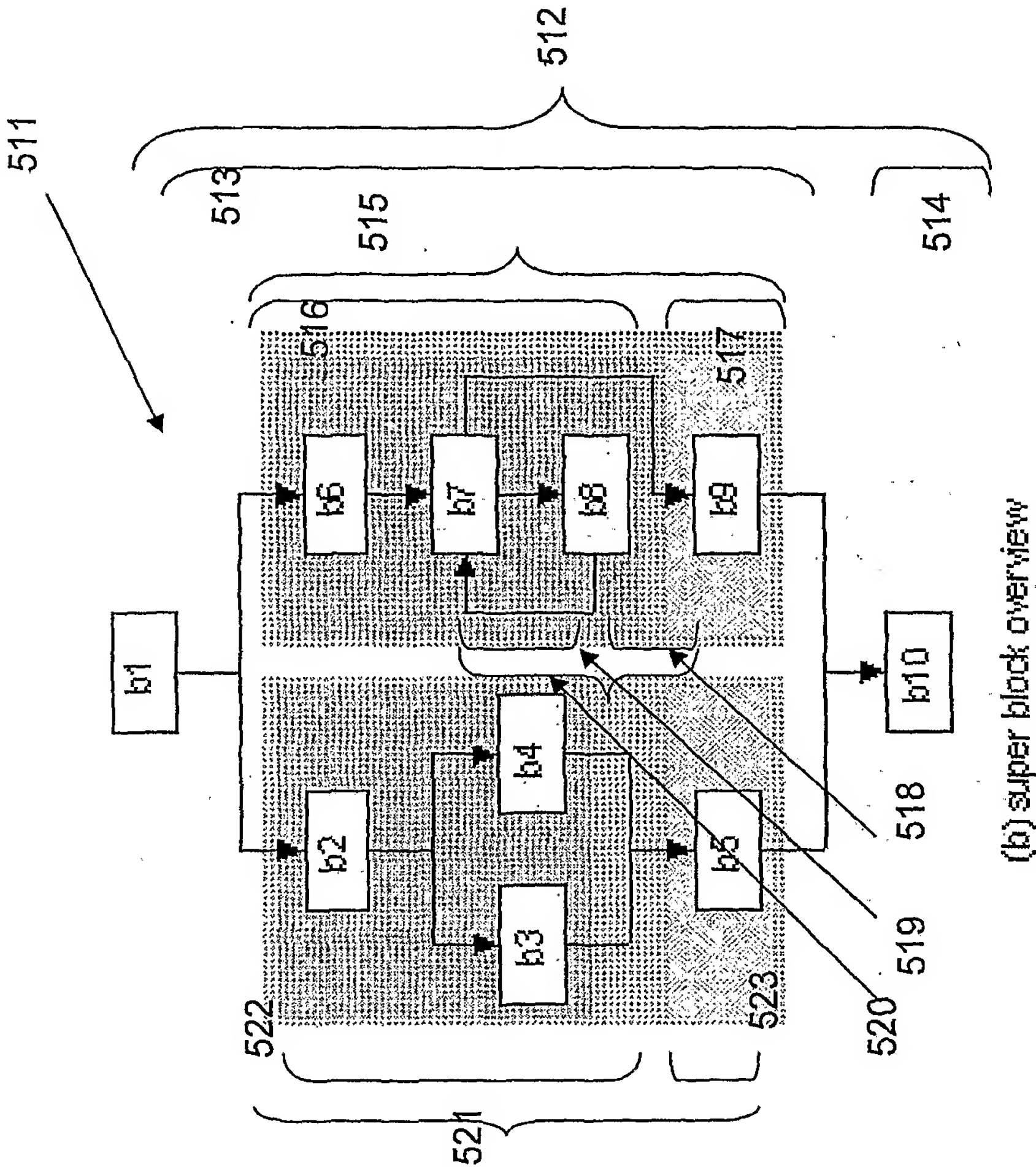
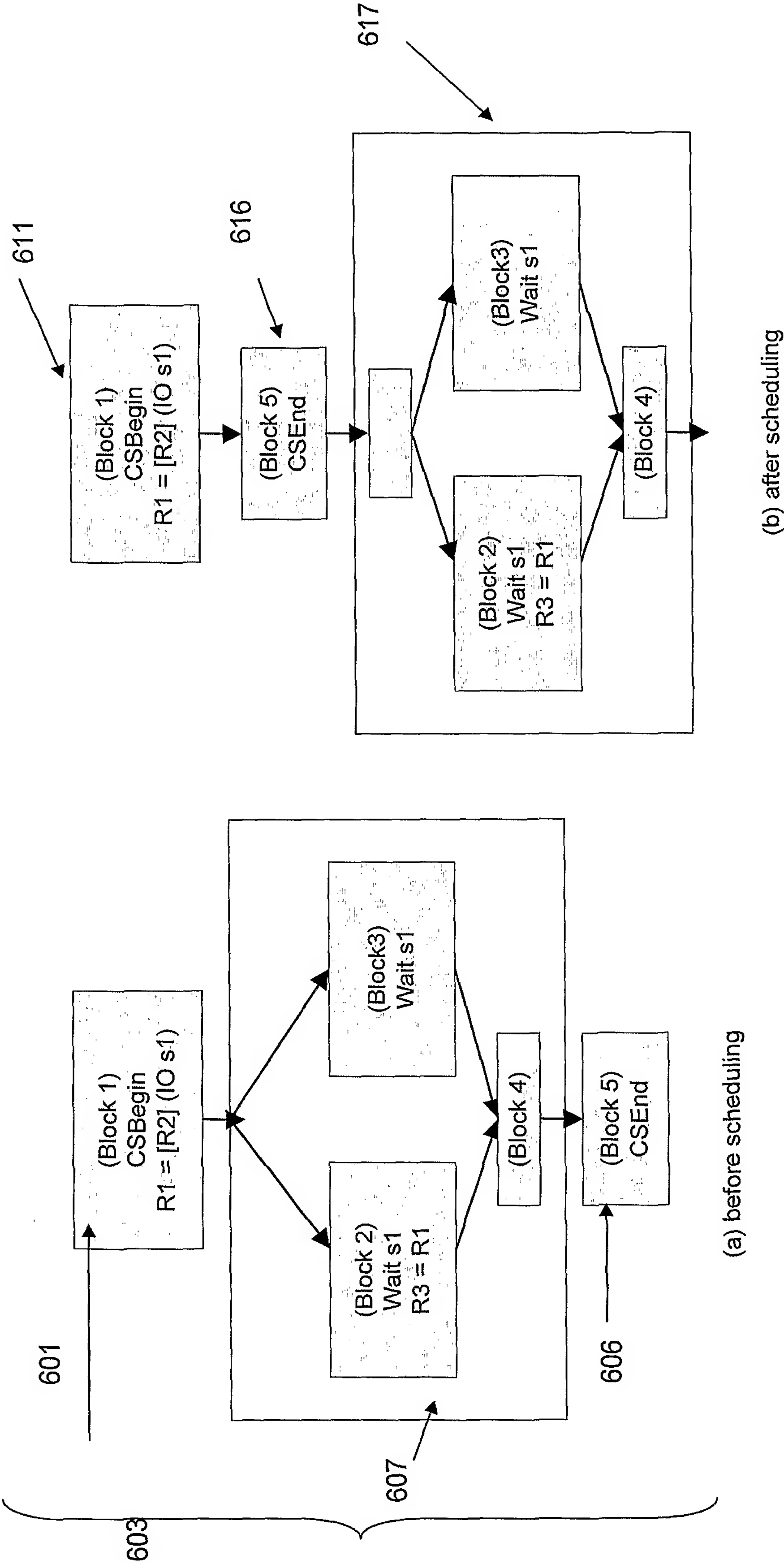


Fig. 5a

Fig. 5b

Figs. 6a-b



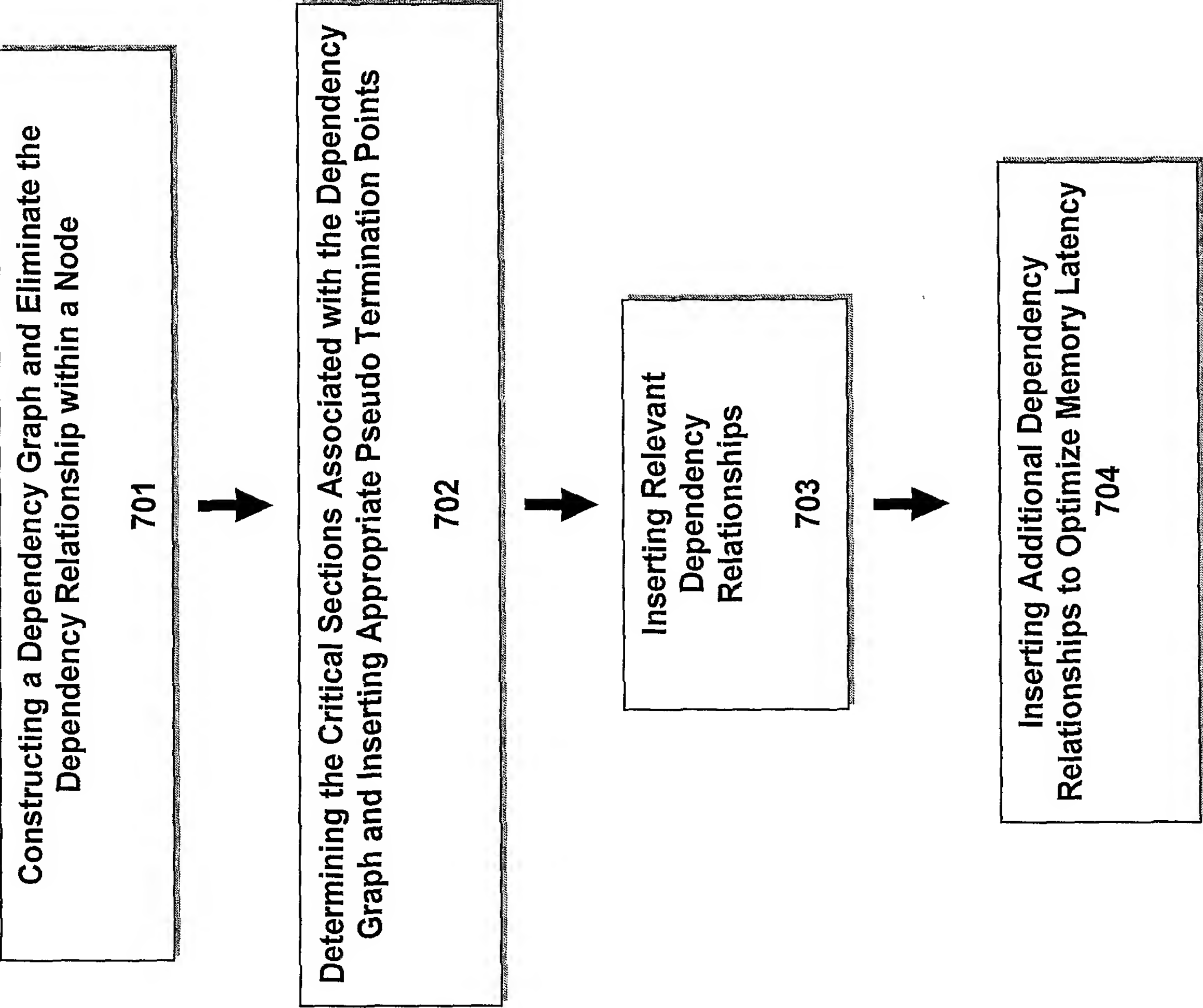


Fig. 8a

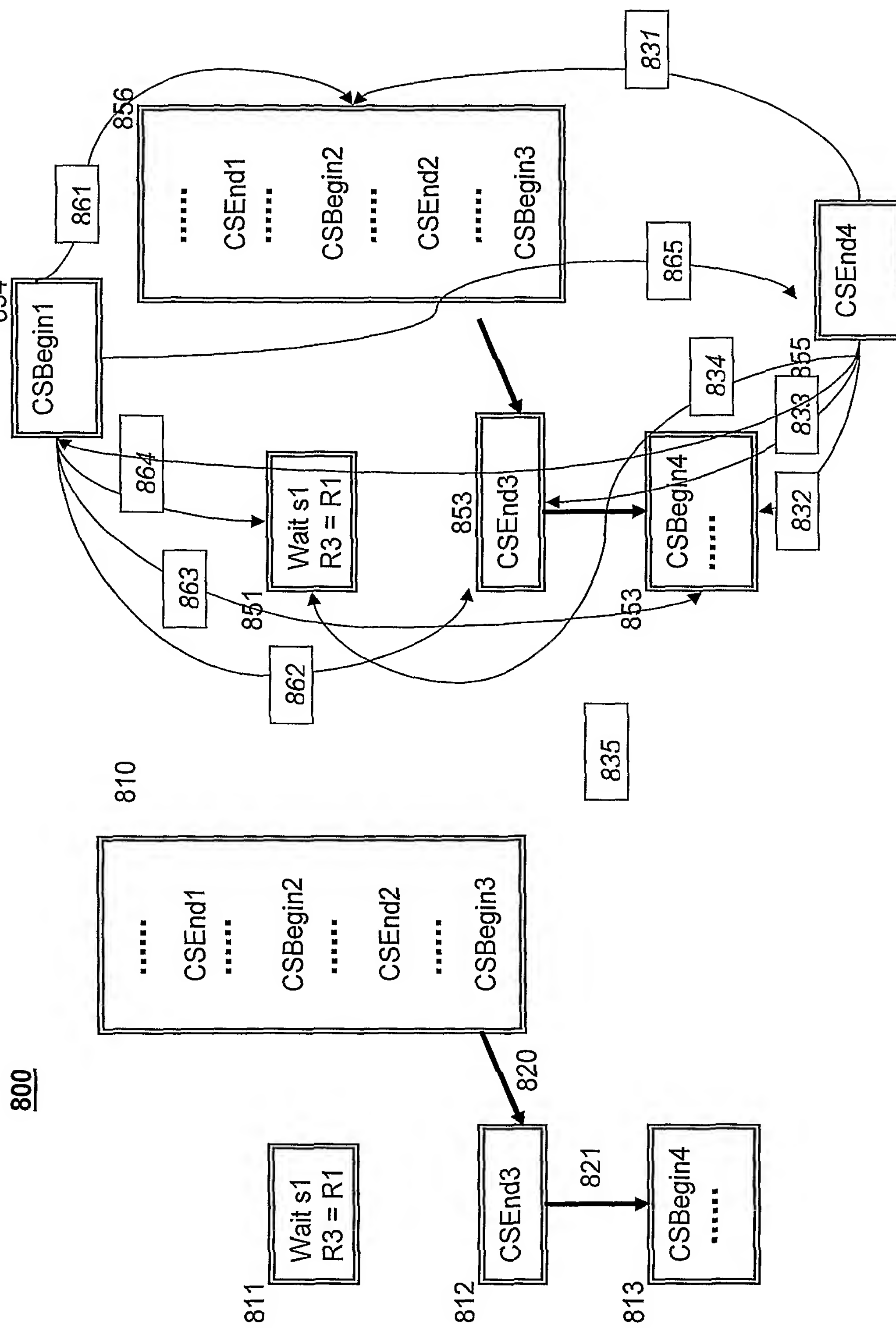


Fig. 8b

870

Fig. 8c

